



PATENT APPLICATION

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U.S. PATENT DOCUMENTS

Exam Init	Ref	Document Number	Issue Date	Name	Class	Sub Class
<u>MK</u>		5,512,860	4-30-96	Huscroft et al.	331/1	331/11; /14; 331/25; 331/DIG.2

FOREIGN PATENT DOCUMENTS

Exam Init	Ref	Document Number	Publication Date	Country	Name
<u>MK</u>		WO 85/00259	1-17-85	DE, DE (European patent), FR (European patent) GB, GB (European patent) JP	M/A-COM DCC, Inc..
<u>MK</u>		WO04/102403 A2	11-25-04	All designated states	Advanced Micro Devices, Inc.

OTHER DOCUMENTS

Exam Init	Ref	Author, Title, Date, Pertinent Pages, Etc.)
<u>MK</u>		"IEEE std 1596.4-1996 - IEEE Standard for High-Bandwidth Memory Interface Based on Scalable Coherent Interface (SCI) Signaling Technology (RamLink)" IEEE STD 1596.4-1995, XX, XX 31 December 1996 (1996-12-31), pages 1-91, XP002333770, figure 14, page 44

Examiner: /Michael Krofcheck/

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